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**U.S. UTILITY Patent Application**

C.I.P.E.		PATENT DATE	
SCANNED	DOWN Q.A.	4/1	100
CLASS 427	SUBCLASS 100	ART UNIT 1762	EXAMINER J. B.

APPLICANTS

## TECHNICAL FIELD

Technical field: The present invention relates to methods for forming capacitors on semiconductor substrates using same.

PTO-2040  
12/99

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
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